

Dual-Transformer based symmetrical Triple-Port Active Bridge (DT-ATAB) Isolated DC-DC Converter

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ABSTRACT: Dual-transformer based mostly asymmetrical triple-port active bridge convertor (DT-ATAB) is projected to interface 2 completely different dc-sources and a load. The convertor combines a slow primary supply and a quick storage to power a standard load. Since this sort of system is gaining quality in property energy generation systems and electrical vehicles, the projected topology is of sensible interest. The projected convertor consists of 3 high-frequency convertor stages operational during a six-step mode, and a high-frequency three-port three-phase symmetrical electrical device. The convertor provides galvanic isolation and supports biface power flow for all the 3 ports. In three-phase structure, the present handling capability of the circuit is larger and also the ripple currents at the dc sides area unit abundant lower attributable to the interleaving impact of the only part.

I. INTRODUCTION

The buck boost convertor could be a DC to DC convertor. The output voltage of the DC to DC convertor is a smaller amount than or bigger than the input voltage. The output voltage of the magnitude depends on the duty cycle. These converters {are also |also area unit are} called the maximize and step down electrical devices and these names are returning from the analogous maximize and step-down transformer. The input voltages area unit step up/down to some level of quite or but the input voltage. By mistreatment the low conversion energy, the input power is adequate to the output power. the subsequent expression shows the low of a conversion. Input power (Pin) = Output power (Pout)

For the step-up mode, the input voltage is less than the output voltage ($V_{in} < V_{out}$). It shows that the output current is less than the input current. Hence the buck booster is a step-up mode.

- $V_{in} < V_{out}$ and $I_{in} > I_{out}$

In the step-down mode the input voltage is greater than the output voltage ($V_{in} > V_{out}$). It follows that the output current is greater the input current. Hence the buck boost converter is a step-down mode.

- $V_{in} > V_{out}$ and $I_{in} < I_{out}$

II. BUCK BOOST CONVERTER

It is a sort of DC to DC device and it's a magnitude of output voltage. it should be a lot of or but capable the input voltage magnitude. The buck boost device is capable the fly back circuit and single inductance is employed within the place of the electrical device. There are a unit 2 sorts of devices within the buck boost device that area unit buck device and therefore the alternative one is boost converter. These converters will turn out the vary of output voltage than the input voltage. the subsequent diagram shows the fundamental buck boost device.

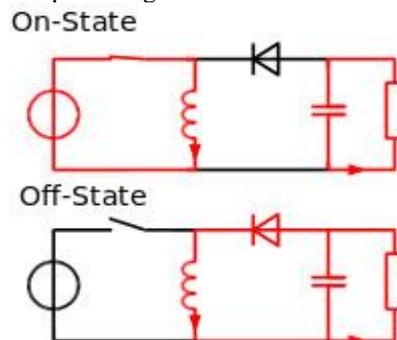


Fig: Buck Boost Converter

Working principle of Buck Boost Converter: The operating operation of the DC to DC device is that the inductance within the input resistance has the sudden variation within the input current. If the switch is ON then the inductance feed the energy from the input and it stores the energy of magnetic energy. If the switch is closed it discharges the energy. The output circuit of the capacitance is assumed as high adequate than the time constant of Associate in Nursing RC circuit is high on the output stage. the large time constant is compared with the switch amount and certify that the steady state may be a constant output voltage $V_o(t) = V_o(\text{constant})$ and gift at the load terminal.

Modes of Buck Boost Converters

There are a unit 2 differing types of modes within the buck boost device. the subsequent area unit the 2 differing types of buck boost converters.

- Continuous conduction mode.
- Discontinuous conduction mode.

Continuous Conduction Mode: In the continuous physical phenomenon mode this from finish to finish of inductance ne'er goes to zero. therefore, the inductance part discharges sooner than the switch cycle.

Discontinuous Conduction Mode: In this mode this through the inductance goes to zero. therefore, the inductance can wholly discharge at the tip of switch cycles.

Applications of Buck boost converter

- It is used in the self-regulating power supplies.
- It has consumer electronics.
- It is used in the Battery power systems.

Advantages of Buck Boost Converter

- It gives higher output voltage.
- Low operating duct cycle.
- Low voltage on MOSFETs

Existing System: moong the offered isolated MPC topologies, triple-port active bridge converters (TAB) area unit one among the foremost eminent topologies which provide most of the same key options. TABs area unit principally appropriate for hybrid and electrical vehicle applications wherever MPCs area unit used because the interfacing between dc sources, storage devices, and load. However, TABs don't seem to be appropriate for the applications wherever 2 totally different sources area unit connected to produce the load power at the same time. as a result of atiny low distinction within the magnitudes and/or phases between their corresponding high-frequency (HF) ac voltages could result in higher current powers between the supply ports thanks to the smaller discharge inductance between their interconnecting windings of the electrical device, also, the upper current powers lead to higher losses and therefore the deterioration of the electrical device core still as windings. although this MPC minimizes the current currents, what is more, during this device, isolation of any supply port throughout operation can lead to either the hyperbolic losses or inadequate outputs.

III. DISADVANTAGES:

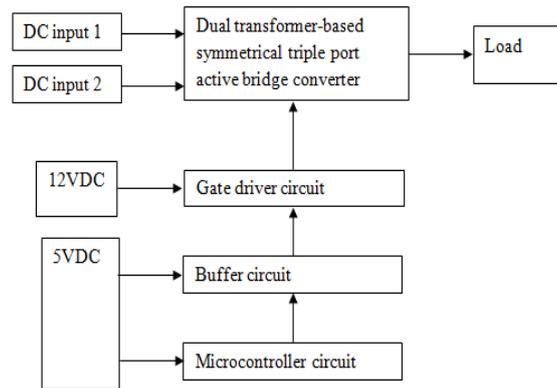
- The higher circulating powers result in higher losses.
- It does not offer bidirectional power flow

Proposed System: Three-port three-phase device topology appropriate for power management is projected, and its potential for high-octane applications is investigated. The device isa direct extension of the single-phase version of the 3-port device. It consists of 3 active power electronic converters and 2 high-frequency transformers. All switches of those converters are turned-on with zero-voltage-switching (ZVS) to cut back the switch losses. The biface power flow operation is feasible between the ports. It conjointly reduces the current powers between the ports for compatible electrical device turns ratios as compared to those within the different existing triple-port active bridge converters (TAB).

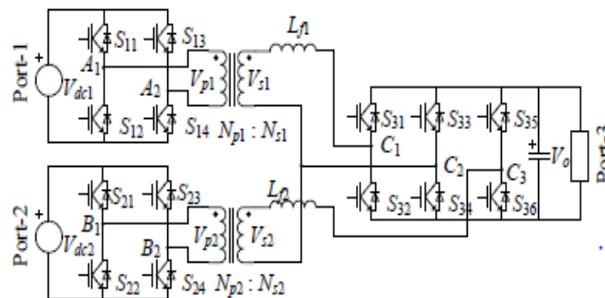
Advantages:

- Maintain the load voltage at the desired level and to regulate the powers supplied from the sources.
- Minimizes the leakage power between the source ports.

Block Diagram



Circuit Diagram:



Capacitors: A condenser is solely 2 items of metal close to one another, separated by associate nonconductor or air. A condenser is employed to store charge and energy.

A parallel-plate condenser consists of 2 parallel plates separated by a distance d , every plate with space A . If A is massive and d is little, the plates square measure effectively infinite planes, and also the E-field is uniform and fully middle the plates.

S.N.	Fabrication Technique	Application
1	Alloy Junction: A small dot of In is kept on n type Si and heated to 150oC. It melts and dissolves. The temperature is then lowered.	High PIV diode
2.	Point Contact: The junction area is kept very small so that the capacitance value is low.	High Frequency (10Ghz)
3.	Epitaxial Growth: Junction is fabricated on an epitaxial layer.	Low Resistance
4.	Grown Junction: Czochralski technique where a single semiconductor seed which is immersed in molten semiconductor material is gradually with drawn with the help of a rod which holds the seed. pn junction is fabricated by first adding p type and then n type impurity.	High current application because of high area of contact.

Table: gives selected a few techniques and applications.

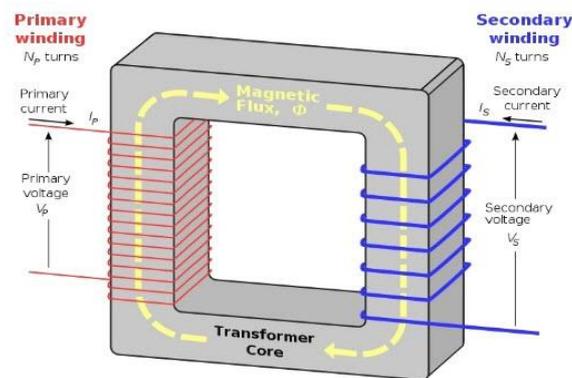
Transformer: The electrical device could be a static device, that contains of 1 or additional windings that square measure coupled magnetically and separated electrically with a core. It transmits the power from one circuit to the opposite by the principle of magnetic force induction. the first winding is outlined as, the winding connected to the most offer, whereas, the coil is that the winding is connected to the load. the 2 windings with acceptable insulation square measure wound on a coated core that provides a magnetic pathway between



windings.

Fig: Transformer

Construction of Transformer: The essential parts of a transformer mainly include the primary winding, secondary winding and magnetic core

**Fig: Construction of Transformer**

Primary Winding of Transformer: this kind of electrical device produces magnetic flux once it's related to the electrical supply. **Magnetic Core of Transformer:** during this sort of electrical device, the magnetic flux created by the most winding, that may allow through this low reluctance track connected with coil and build a closed magnetic circuit. **Secondary Winding of Transformer:** during this sort of electrical device, the flux created by the first winding that passes through the core which can connect with the coil. This winding conjointly winds on the similar core and provides the well-liked o/p of the electrical device.

IV. PIC CONTROLLER

High-Performance RISC CPU:

Only 35 single-word instructions to learn

All single-cycle instructions except for program branches, which are two-cycle

Operating speed: DC – 20 MHz clock input DC – 200 ns instruction cycle

Up to 8K x 14 words of Flash Program Memory, Up to 368 x 8 bytes of Data Memory (RAM), Up to 256 x 8 bytes of EEPROM Data Memory

Pin out compatible to other 28-pin or 40/44-pin

PIC16CXXX and PIC16FXXX microcontrollers

Peripheral Features:

Timer0: 8-bit timer/counter with 8-bit prescaler

Timer1: 16-bit timer/counter with prescaler, can be incremented during Sleep via external crystal/clock

Timer2: 8-bit timer/counter with 8-bit period register, prescaler and postscaler

Two Capture, Compare, PWM modules

Capture is 16-bit, max. resolution is 12.5 ns

Compare is 16-bit, max. resolution is 200 ns

PWM max. resolution is 10-bit

Synchronous Serial Port (SSP) with SPI™ (Master mode) and I2C™ (Master/Slave)

Universal Synchronous Asynchronous Receiver Transmitter (USART/SCI) with 9-bit address detection
Parallel Slave Port (PSP) – 8 bits wide with external RD, WR and CS controls (40/44-pin only)
Brown-out detection circuitry for Brown-out Reset (BOR)

Special Microcontroller Features:

100,000 erase/write cycle Enhanced Flash program memory typical
1,000,000 erase/write cycle Data EEPROM memory typical
Data EEPROM Retention > 40 years
Self-reprogrammable under software control
In-Circuit Serial Programming™ (ICSP™) via two pins
Single-supply 5V In-Circuit Serial Programming
Watchdog Timer (WDT) with its own on-chip RC oscillator for reliable operation
Programmable code protection
Power saving Sleep mode
Selectable oscillator options
In-Circuit Debug (ICD) via two pins

CMOS Technology:

Low-power, high-speed Flash/EEPROM technology
Fully static design
Wide operating voltage range (2.0V to 5.5V) Commercial and Industrial temperature ranges Low-power consumption.

V. CONCLUSION

Thus, the Dual-transformer based mostly asymmetrical triple-port active bridge convertor (DT-ATAB) is planned to interface 2 totally different dc-sources and a load. The convertor combines a slow primary supply and a quick storage to power a standard load. Since this sort of system is gaining quality in property energy generation systems and electrical vehicles, the planned topology is of sensible interest. The planned convertor consists of 3 high-frequency convertor stages in operation in a very six-step mode, and a high-frequency three-port three-phase symmetrical electrical device. The convertor provides galvanic isolation and supports two-way power flow for all the 3 ports. In three-phase structure, this handling capability of the circuit is larger and also the ripple currents at the dc sides are abundant lower because of the interleaving impact of the one part.

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